

OVERVIEW

Carbon Model Studio generates models which integrate directly into OSCI SystemC™ and other SystemC simulators. The Carbon/SystemC combination enables designers to rapidly develop and assemble complete virtual platforms which can be used to explore architectural tradeoffs and perform pre-silicon software development.

RAPID DEVELOPMENT OF SYSTEMC COMPONENTS

Components generated by Carbon Model Studio automatically plug directly into SystemC. Carbon Model Studio's straightforward graphical user interface enables the user to quickly create a Carbon model, configure its ports to match the data types for your SystemC environment and enable software visible registers.

ACCELERATED SOFTWARE DEBUG AND PROFILING

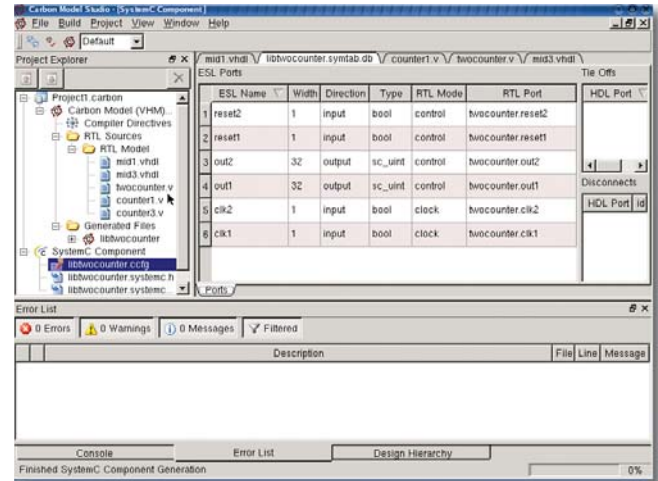
SystemC Components generated by Carbon Model Studio enable SystemC platforms to be assembled rapidly and accurately. Software engineers have complete visibility into all of the Carbon Model's software visible registers and memories. In addition, all Carbon Models have the capability to dump waveforms. This enables the software and hardware engineer to work together to debug complex system problems or any hardware problems which are uncovered by running software.

ABOUT CARBON MODEL STUDIO

Carbon Model Studio is a complete solution for the automatic generation, validation, and execution of hardware-accurate software models. Carbon Model Studio enables you to begin the development and debug of software before silicon is available. System architects can use Carbon Model Studio for architectural analysis and profiling. Software engineers can develop and debug embedded software, firmware, drivers and diagnostics concurrent with hardware development. Additionally, Carbon Models can be securely distributed to third parties to accelerate adoption of your technology in end devices.

ABOUT SYSTEMC

Ratified as IEEE Std. 1666™-2005, SystemC™ is a language built in standard C++ by extending the language with the use of class libraries. SystemC addresses the need for a system design and verification language that spans hardware and software. The language is particularly suited to model system's partitioning,



Generating a SystemC Component with Carbon Model Studio.

to evaluate and verify the assignment of blocks to either hardware or software implementations, and to architect and measure the interactions between and among functional blocks.

ABOUT CARBON DESIGN SYSTEMS

Carbon is the leading supplier of system-level tools to automatically create, validate, and deploy software models generated from Verilog® and/or VHDL.

Carbon allows you to leverage your existing IP for architectural analysis and hardware / software validation. We give you the keys to guarantee first pass system success by accelerating hardware / software integration.

First pass silicon, first pass software: first pass system success.

For more information, visit us at:

www.carbondesignsystems.com

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