



News Release

Waltham, MA,

**Carbon Design Systems Continues Popular Webinar Series
with Two Upcoming Events:**

*Profiling Your Design on April 30th;
Hardware and Software Validation on May 8th*

Who:

Carbon Design Systems is the leading supplier of system-level tools for the creation, validation and deployment of cycle-accurate hardware models automatically generated from Verilog and/or VHDL design descriptions. These models are used in conjunction with popular simulation platforms for architecture profiling and software validation in parallel with hardware development.

What:

At the Profiling Webinar, attendees learn how to optimize architecture, software and hardware boundaries, chip performance, processor selection and cache organization.

At the Validation Webinar, attendees learn how to get answers before first silicon or FPGA prototyping; how to develop, debug and test software; solutions for multi-core debugging and how to do fast and accurate system validation.

When:

Profiling Your Next Design Webinar

Monday, April 30, 2007
11:00 am PDT / 2:00 pm EDT

Validating Chip Software and Hardware Webinar

Tuesday, May 8, 2007
11:00 am PDT / 2:00 pm EDT

Where:

Online webinar through web browser.

Webinar Registration:

Join the upcoming Carbon webinars, by registering at: PROFILING for the April 30th Profiling Your Design webinar and/or at CHIP VALIDATION for the May 8th Validating Chip Software and Hardware webinar.

About Carbon

Carbon is delivering a high-performance virtual system prototyping solution that enables an ASIC or SoC prototype to be rapidly assembled and functionally validated on an engineer's desktop months before silicon. Carbon's new software approach allows multiple levels of abstraction to be validated together including processors, peripherals, C, SystemC, Verilog, VHDL, IP cores, and transaction-level models. The key to VSP is silicon accuracy and performance -- the ability to execute billions of cycles and boot embedded operating systems, all with desktop software. Problems can be found and resolved during the design cycle -- rather than waiting for idealized behavioral models to be developed or first silicon to be delivered.

The company is headquartered at 375 Totten Pond Road, Suite 100/200, Waltham, MA. 02451.
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